

1/13

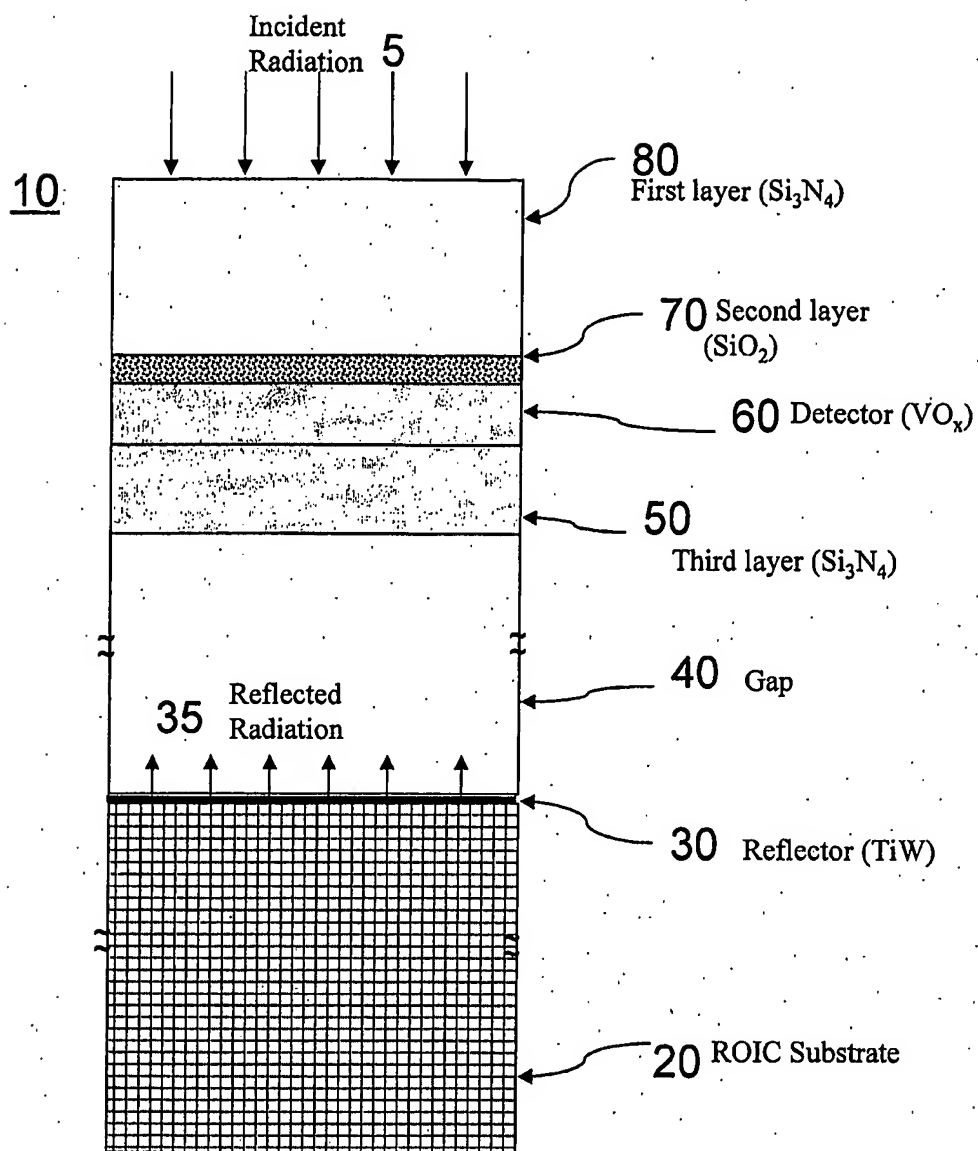


Figure 1
(PRIOR ART)

2/13

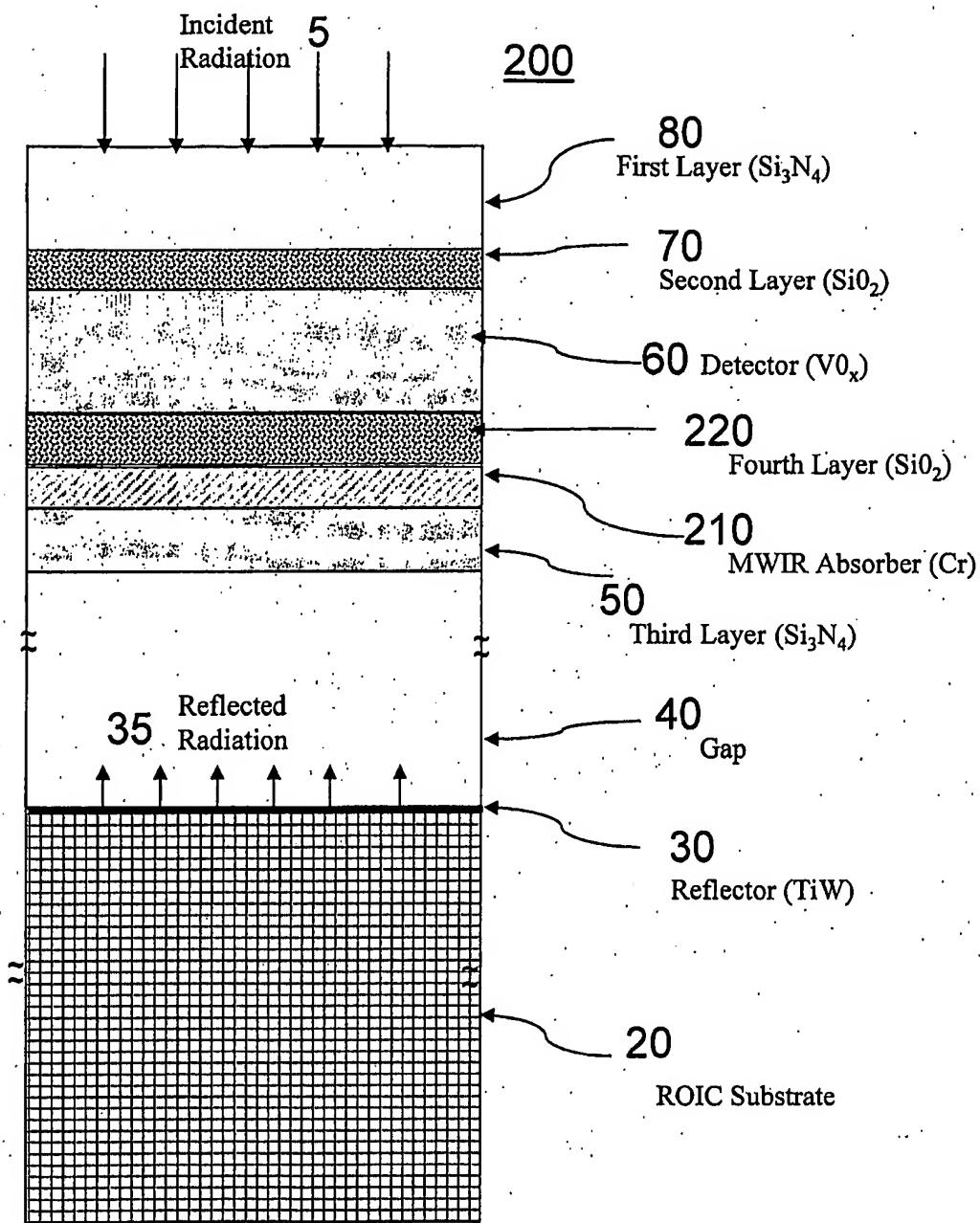


Figure 2

3/13

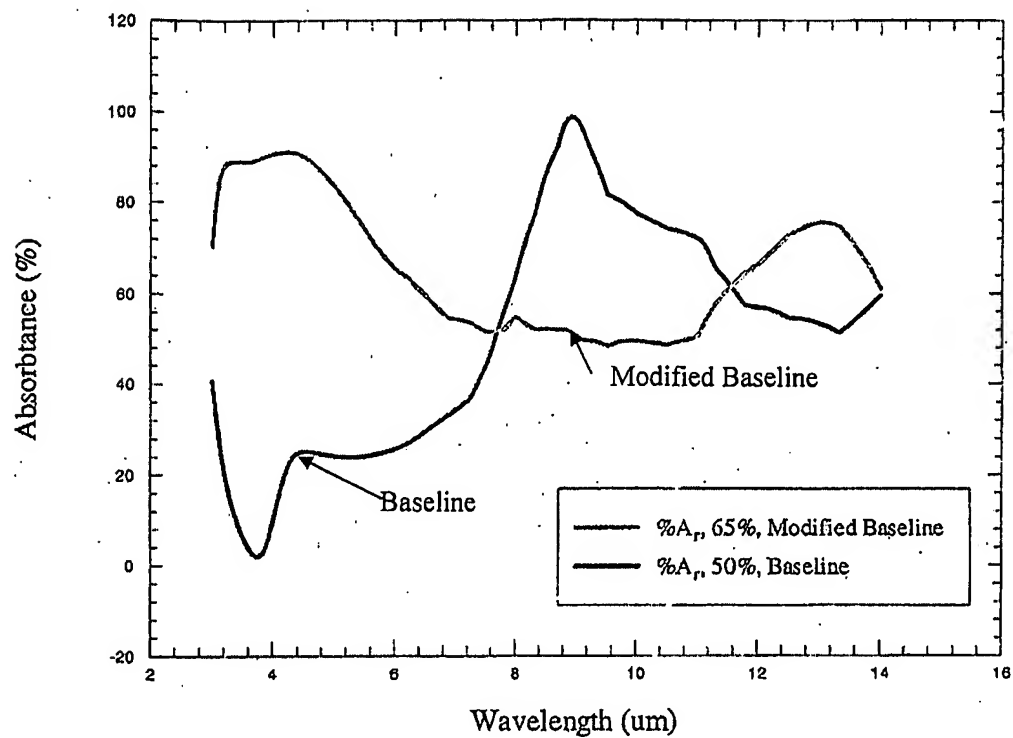


Figure 3

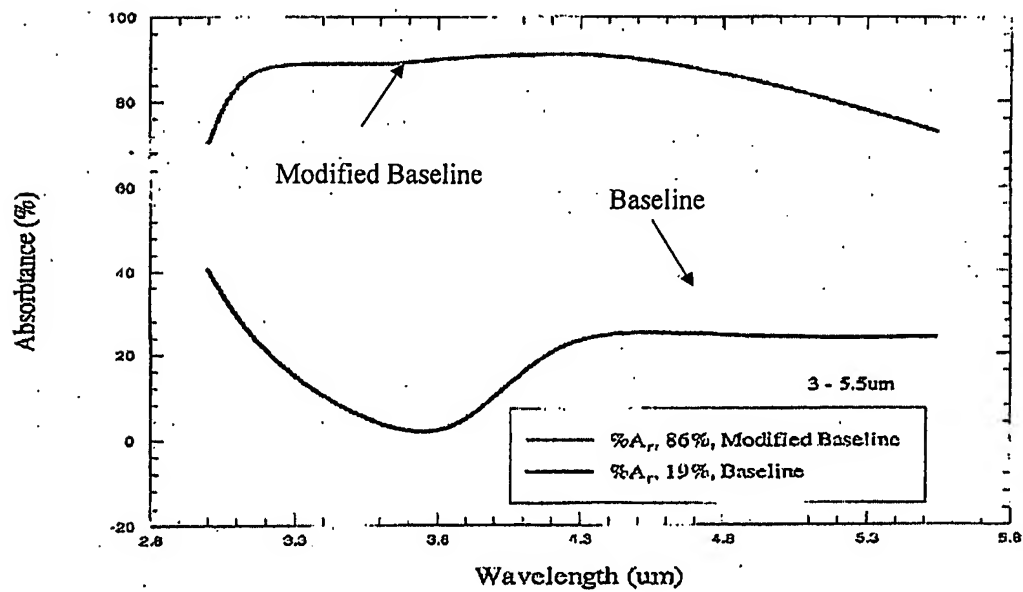


Figure 4

4/13

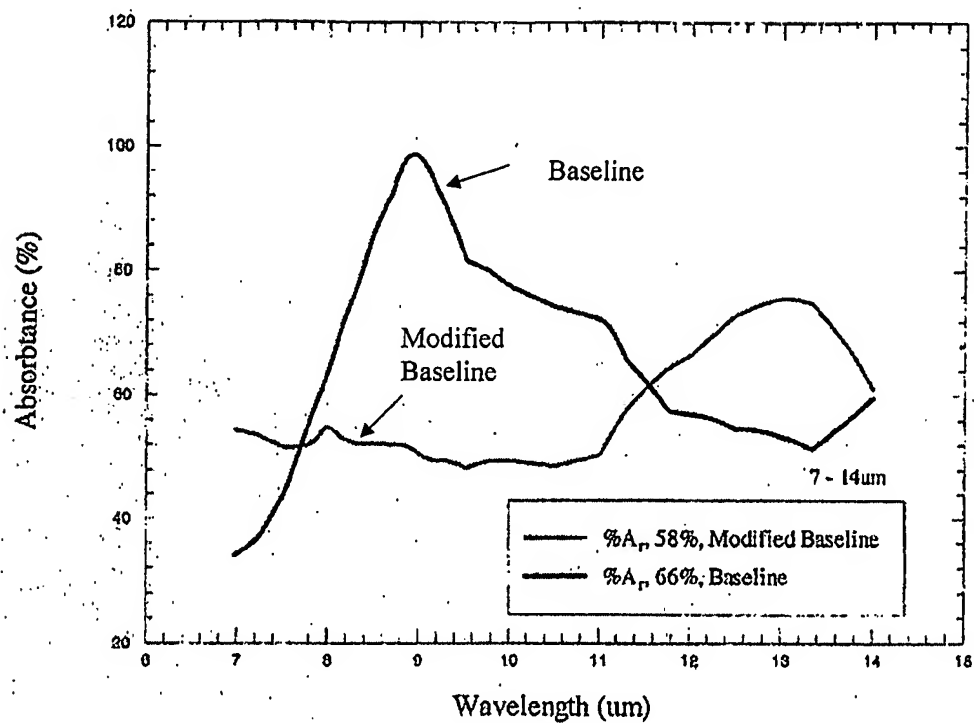


Figure 5

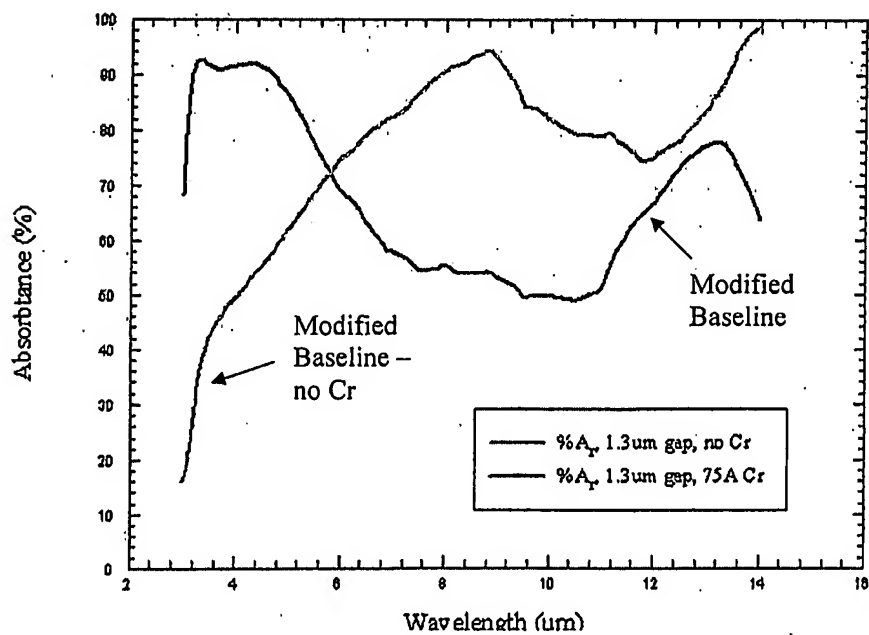


Figure 6

5/13

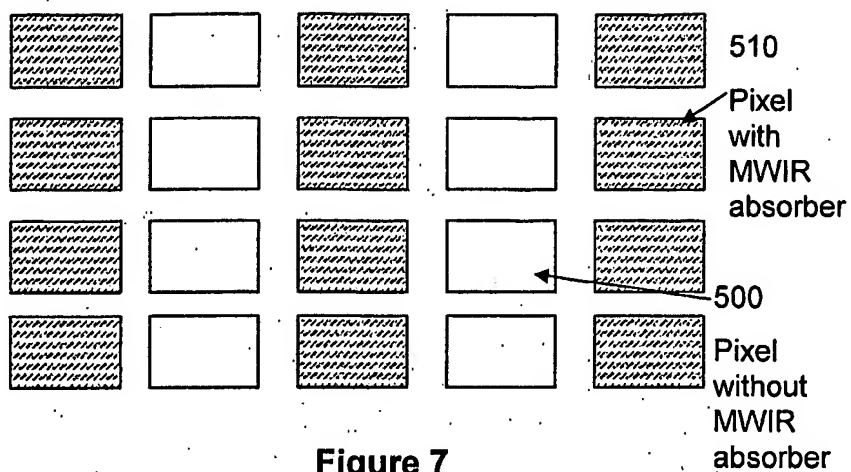


Figure 7

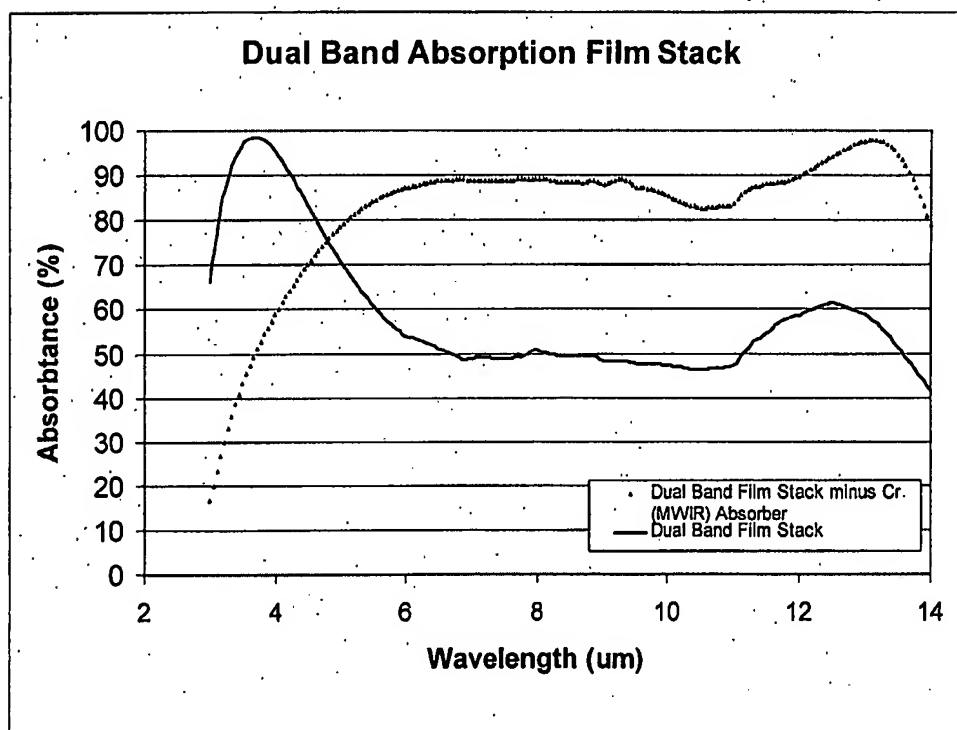


Figure 8

6/13

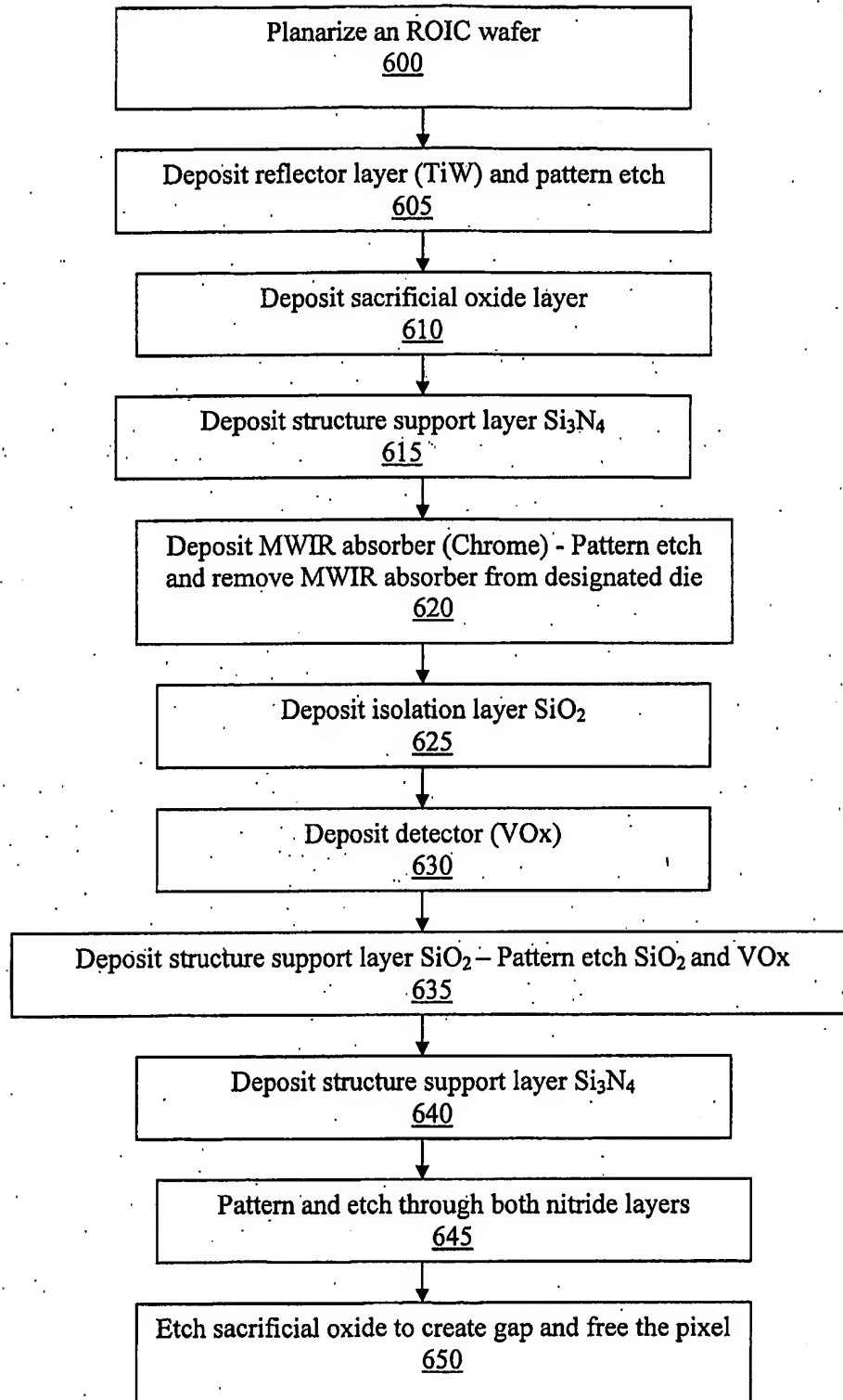


Figure 9

7/13

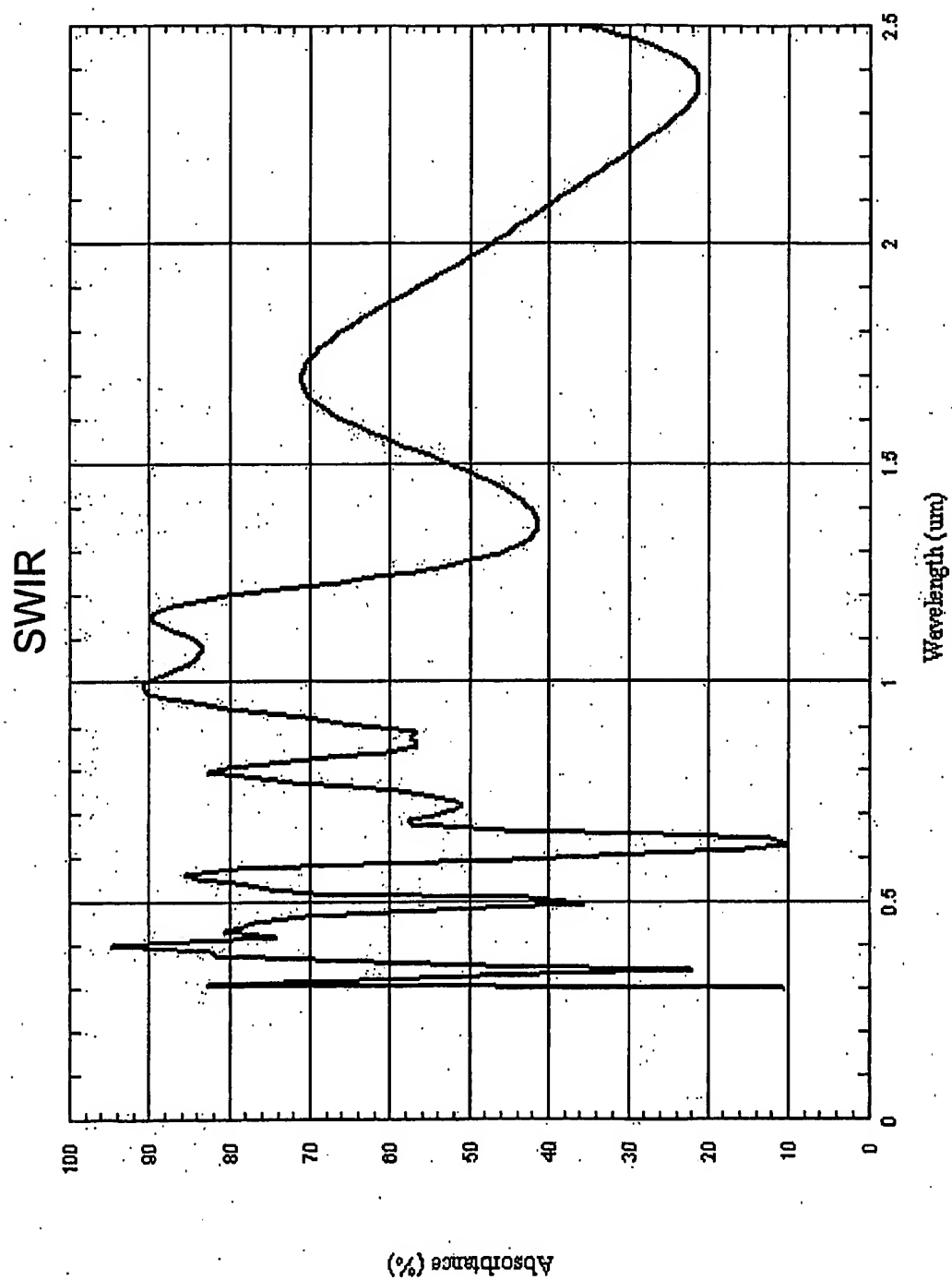


Figure 10a

8/13

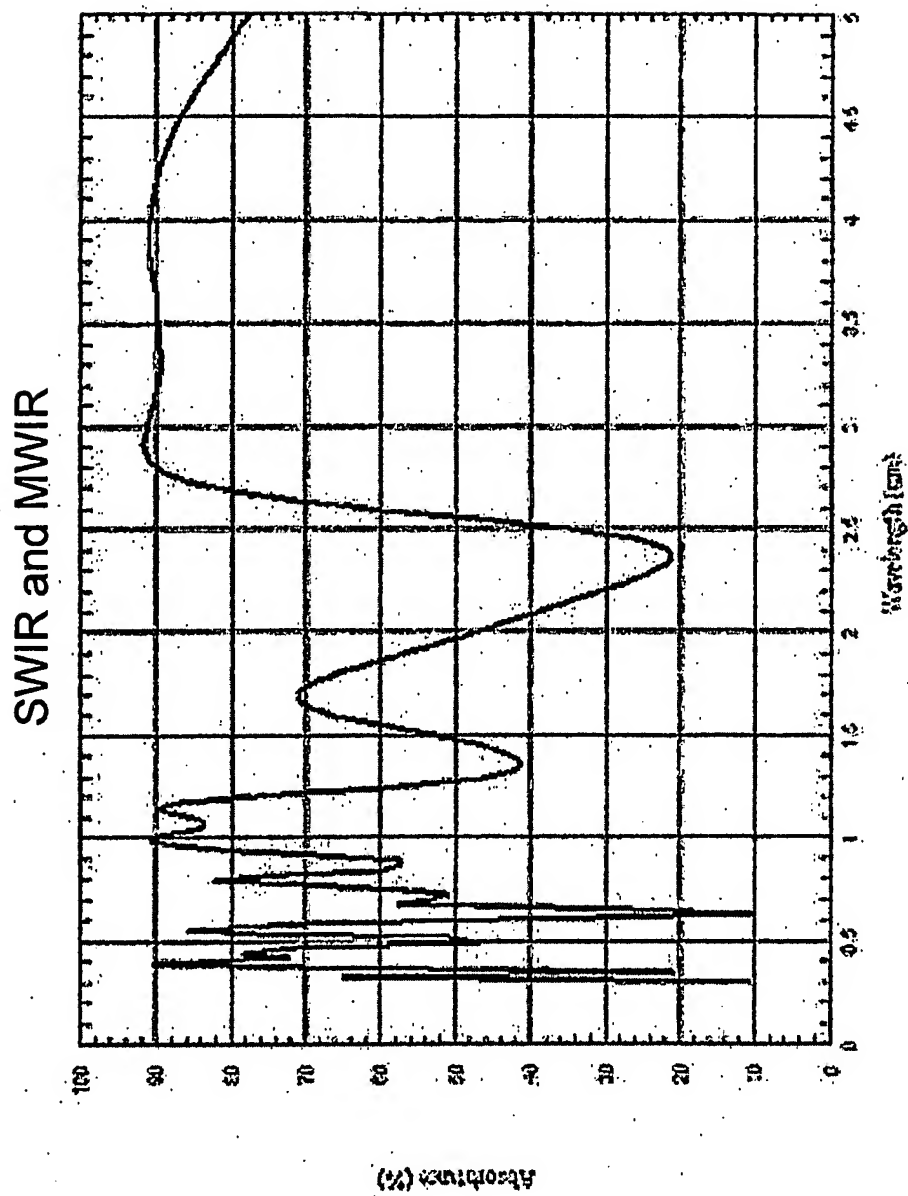


Figure 10b

9/13

SWIR, MWIR, and LWIR

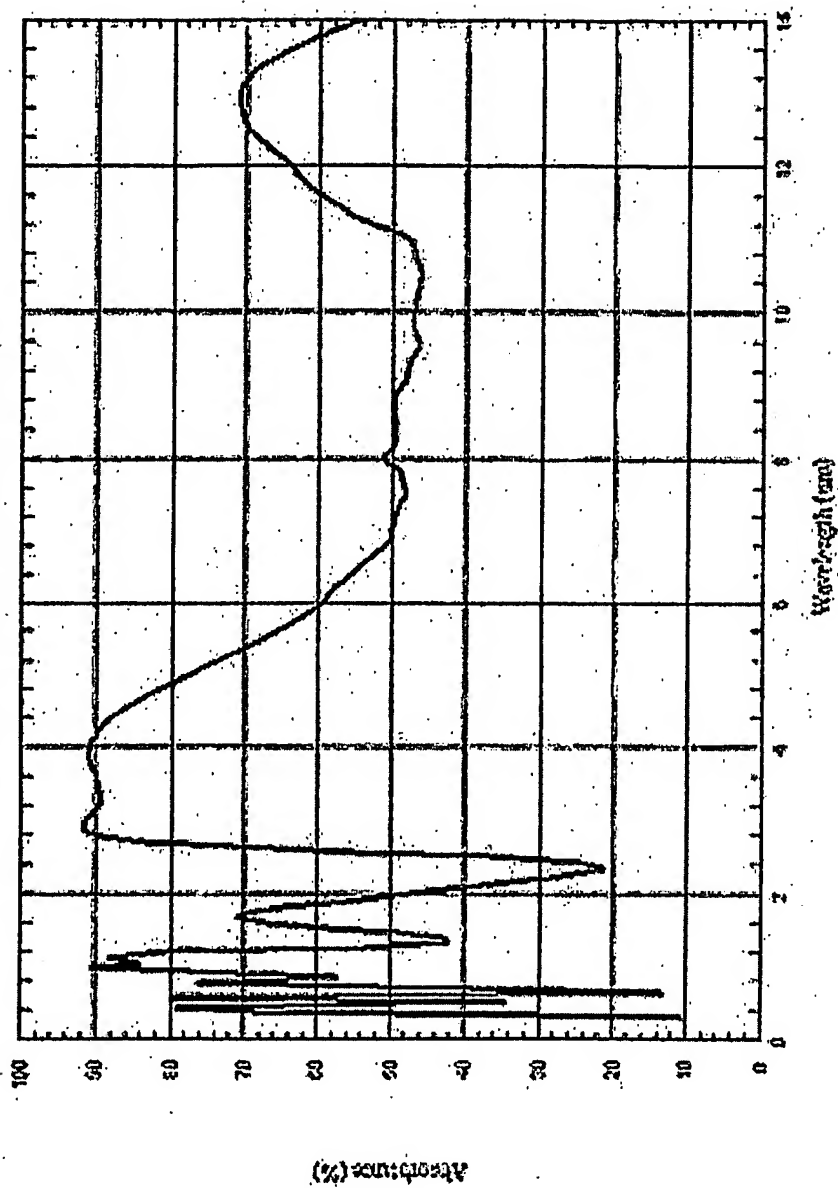


Figure 10c

10/13

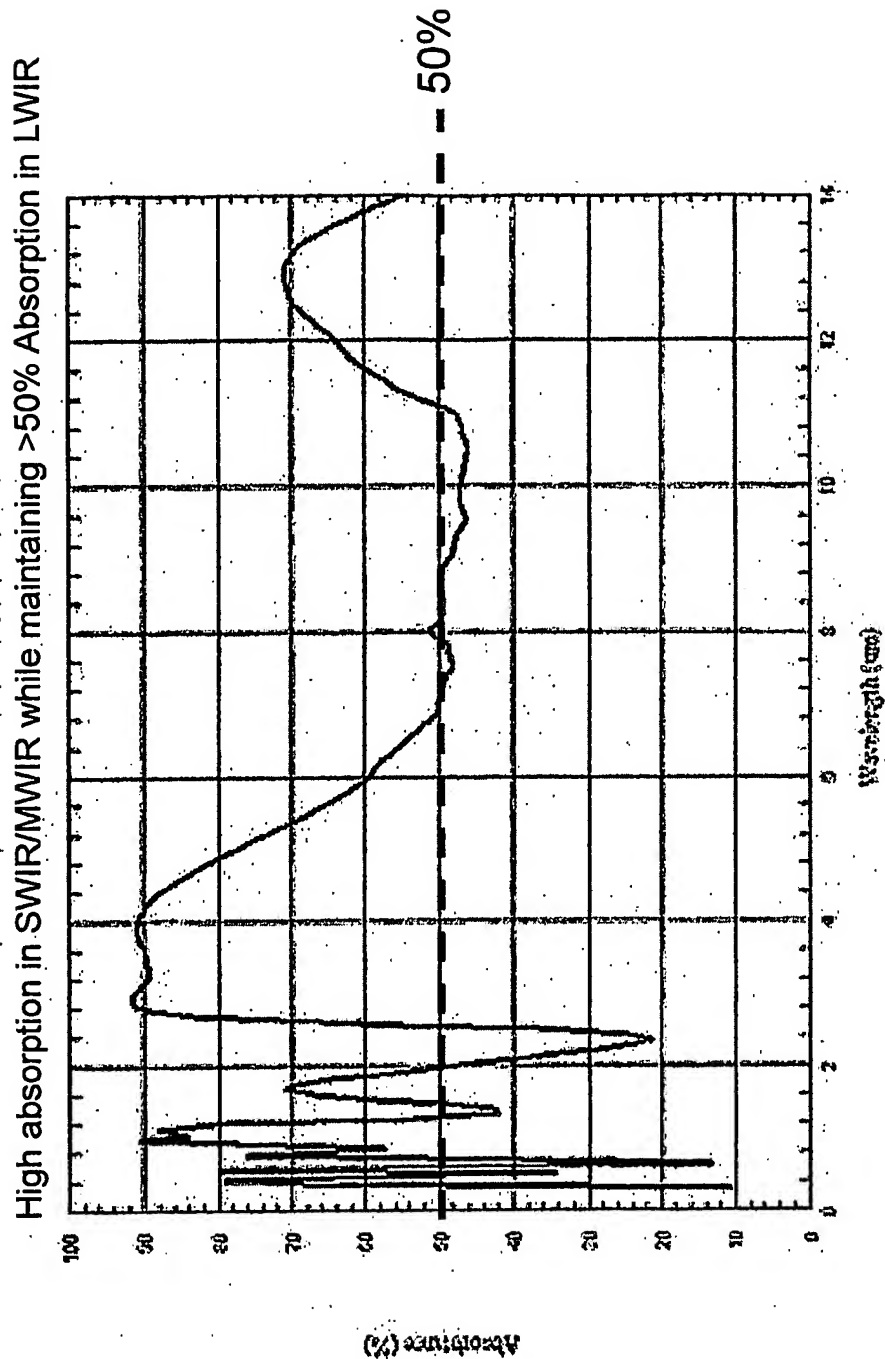


Figure 10d

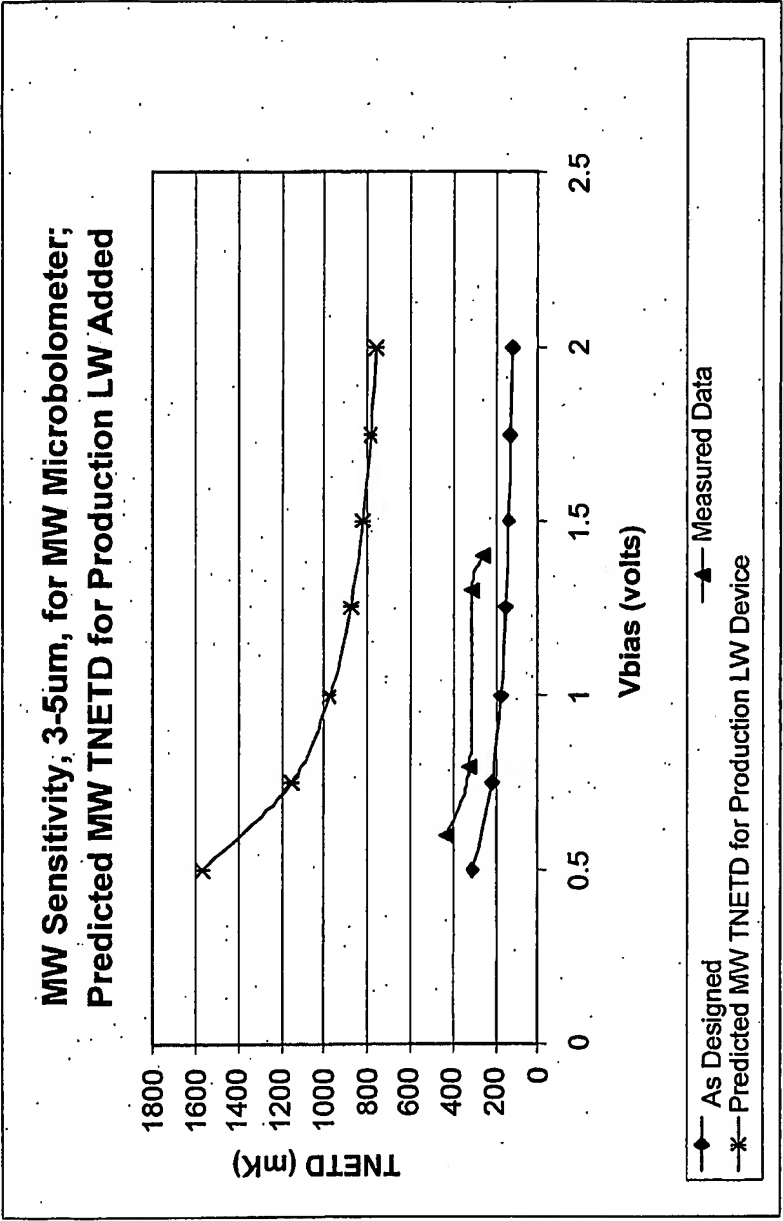


Figure 11

12/13

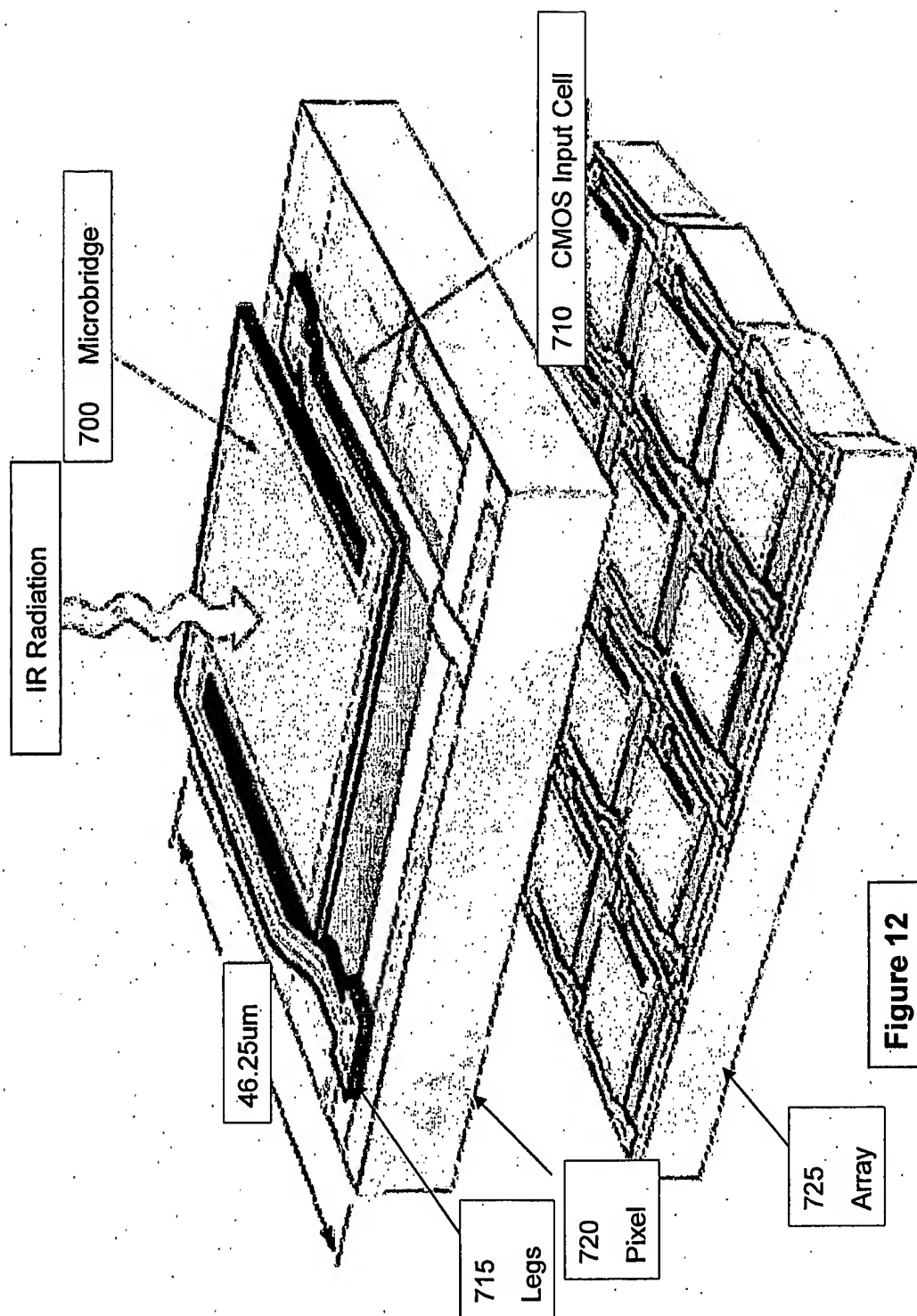


Figure 12



Figure 13